



**SN54LS/74LS795
SN54LS/74LS796
SN54LS/74LS797
SN54LS/74LS798**

TRI-STATE OCTAL BUFFERS

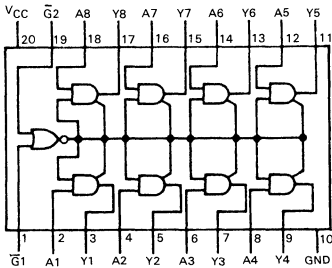
LOW POWER SCHOTTKY

DESCRIPTION — The SN54LS/74LS795 thru SN54LS/74LS798 device types provide a second source for the 71/81LS95 thru 71/81LS98 series. These devices are octal low power Schottky versions of the 70/8095 thru 70/8098 3-STATE Hex Buffers. The LS795 and LS797 are noninverting and the LS796 and LS798 are inverting functions. On each buffer, one of the two inputs is used as a control line to gate the output into the high impedance state, while the other input passes the data through the buffer. On the LS795 and LS796 access is through a 2-input NOR gate, with all eight 3-STATE enable lines common. On the LS797 and LS798, four buffers are enabled from one common line and the other four buffers from another common line. On all device types the 3-STATE condition is achieved by applying a high logic level to the enable pins.

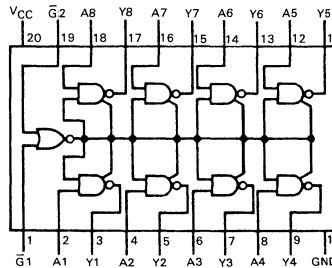
TRUTH TABLES

LS795				LS796				LS797			LS798		
INPUTS		OUTPUT		INPUTS		OUTPUT		INPUTS	OUTPUT		INPUTS	OUTPUT	
$\bar{G}1$	$\bar{G}2$	A	Y	$\bar{G}1$	$\bar{G}2$	A	Y	\bar{G}	A	Y	\bar{G}	A	Y
H	X	X	Z	H	X	X	Z	H	X	Z	H	X	Z
X	H	X	Z	X	H	X	Z	L	H	H	L	H	L
L	L	H	H	L	L	H	L	L	L	L	L	L	H
L	L	L	L	L	L	L	H						

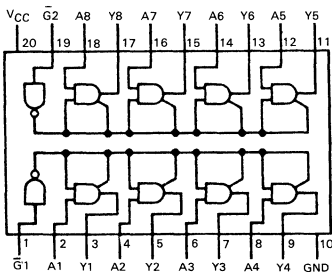
LOGIC DIAGRAMS



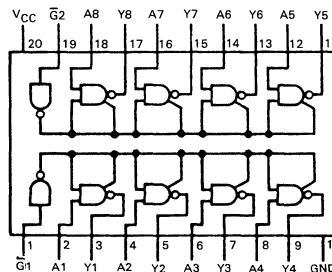
SN54LS/74LS795



SN54LS/74LS796



SN54LS/74LS797



SN54LS/74LS798

J Suffix — Case 732-03 (Ceramic)
N Suffix — Case 738-01 (Plastic)

GUARANTEED OPERATING RANGES

SYMBOL	PARAMETER		MIN	TYP	MAX	UNIT
V _{CC}	Supply Voltage	54	4.5	5.0	5.5	V
		74	4.75	5.0	5.25	
T _A	Operating Ambient Temperature Range	54	-55	25	125	°C
		74	0	25	70	
I _{OH}	Output Current — High	54			-2.6	mA
		74			-5.0	
I _{OL}	Output Current — Low	54			8.0	mA
		74			16	

DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

SYMBOL	PARAMETER	LIMITS			UNITS	TEST CONDITIONS	
		MIN	TYP	MAX			
V _{IH}	Input HIGH Voltage	2.0			V	Guaranteed Input HIGH Voltage for All Inputs	
V _{IL}	Input LOW Voltage	54		0.7	V	Guaranteed Input LOW Voltage for All Inputs	
		74		0.8			
V _{IK}	Input Clamp Diode Voltage		-0.65	-1.5	V	V _{CC} = MIN, I _{IN} = -18 mA	
V _{OH}	Output HIGH Voltage	54	2.5	3.5	V	V _{CC} = MIN, I _{OH} = MAX	
		74	2.7	3.5	V		
V _{OL}	Output LOW Voltage	54,74		0.25	0.4	V	I _{OL} = 8.0 mA I _{OL} = 16 mA V _{CC} = V _{CC} MIN, V _{IN} = V _{IL} or V _{IH} per Truth Table
		74		0.35	0.5	V	
I _{OZH}	Output Off Current—High			20	μA	V _{CC} = MAX, V _{OUT} = 2.4 V	
I _{OZL}	Output Off Current—Low			-20	μA	V _{CC} = MAX, V _{OUT} = 0.4 V	
I _{IH}	Input HIGH Current			20	μA	V _{CC} = MAX, V _{IN} = 2.7 V	
				-0.1	mA	V _{CC} = MAX, V _{IN} = 7.0 V	
I _{IL}	Input LOW Current A Input, Both \bar{G} at 0.4 V \bar{G} Input			-0.36	mA	V _{CC} = MAX, V _{IN} = 0.4 V	
				-0.36			
I _{OS}	Short Circuit Current			-20	μA	V _{CC} = MAX, V _{IN} = 0.5 V	
				-20			
I _{CC}	Power Supply Current	LS795/LS797			26	mA	V _{CC} = MAX
		LS796/LS798			21		

AC CHARACTERISTICS: T_A = 25°C

SYMBOL	PARAMETER	LIMITS						UNITS	TEST CONDITIONS
		LS795/LS797			LS796/LS798				
		MIN	TYP	MAX	MIN	TYP	MAX		
t _{PLH} t _{PHL}	Propagation Delay		11 15	16 22		6.0 13	10 17	ns	V _{CC} = 5.0 V
t _{PZH} t _{PZL}	Output Enable Time		16 13	25 20		17 16	27 25		
t _{PHZ} t _{PLZ}	Output Disable Time		13 19	20 27		13 18	20 27	ns	C _L = 5.0 pF

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